

# Deliverable #3: Project Schematic

## 1 Objectives

For this deliverable, you will prepare a schematic in EAGLE.

## 2 Document Details

To get full credit, you must create a clean, well organized, and complete schematic of your proposed system.

## 3 Collaboration

The class project is **not** a group project, students will be graded individually based on what they submit, build, test, and finally, demo. All projects must meet the individual requirements listed. However, if you are interested in collaborating with others on a project together, that is fine. Just remember you are responsible for your own grade, so organize appropriately. Make sure your hardware / software / firmware does not depend on someone else's. If you choose to collaborate with others on a larger project, your part must still be able to independently demo a working system that is microcontroller driven, with your own custom software and custom PCB, that includes a radio component, an analog component, and a visual component.

## 4 Submission Instructions

This deliverable is due by 12:01 AM on September 25<sup>th</sup>. Absolutely no late assignments will be accepted.

Submit your document via **handin.cs.clemson.edu**. Do not email me your document.

The document should be an EAGLE document (.sch), no other format will be accepted. Make sure to submit your document using **handin**.

## 5 Grading

This deliverable is worth 13% of your final class grade. Table 1 shows the rubric that you will be graded against.

Table 1: Grading Rubric for Project Schematic

Item	Description	Pts
<i>Frame, Name &amp; Title</i>	Use a frame, name the design, and designer.	10
<i>Power Supply</i>	Bypass capacitors, sane power management	20
<i>Microcontroller</i>	Programming lines, RST handled	20
<i>Radio</i>	Included, and connected to Microcontroller.	10
<i>Completion</i>	Are all components described in system design included?	30
<i>Style</i>	All nets labeled	10

**If the document is not turned in on time, you will get a 0%.**